			-	
L Number	Hits	Search Text	DB	Time stamp
- /	2	(Abdo-Ayman-G Mcnairy-Cameron	USPAT;	2003/12/05
1 /		Desai-Piyush Merrelle-Quinn-W).in.	US-PGPUB;	14:34
		_	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	4	("5701409").pn. (GB-2338791-\$.did.)	USPAT;	2003/12/02
*			US-PGPUB;	13:24
1			EPO; JPO;	
			DERWENT;	
F	2	"haragging bug trangagtion"	IBM_TDB USPAT;	2003/12/03
\/ <sup>=</sup>		"harassing bus transaction"	US-PGPUB;	15:30
1			EPO; JPO;	13.30
/			DERWENT;	1
			IBM TDB	
_	240	"stress testing" and bus	USPAT;	2003/12/03
		_	US-PGPUB;	15:30
		•	EPO; JPO;	
			DERWENT;	
			IBM_TDB	1 / /
_	196		USPAT;	2003/12/03
		@ad<=20010328	US-PGPUB;	15:31
			EPO; JPO;	
			DERWENT; IBM TDB	
_	3039	bus adj transaction	USPAT;	2003/12/05
			US-PGPUB;	14:34
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	2553	(bus adj transaction) and @ad<=20010328	USPAT;	2003/12/05
			US-PGPUB; EPO; JPO;	14:35
			DERWENT;	
			IBM TDB	
_	2000	((bus adj transaction) and @ad<=20010328)	USPAT;	2003/12/05
		and (test or error or fault of diagnos\$3	US-PGPUB;	14:40
		or valid\$5)	EPO; JPO;	
	+		DERWENT;	
	16	(((bus adj transaction) and	IBM_TDB USPAT;	2003/12/05
	0 0	@ad<=20010328) and (test or error or	US-PGPUB;	15:28
		fault of diagnos\$3 or valid\$5)) and	EPO; JPO;	
		(trigger adj event)	DERWENT;	
			IBM_TDB	
-	333	, , , , , , , , , , , , , , , , , , , ,	USPAT;	2003/12/05
		@ad<=20010328) and (test or error or	US-PGPUB;	15:47
		fault of diagnos\$3 or valid\$5)) and contention	EPO; JPO; DERWENT;	
		Controlle	IBM TDB	
_	612	716/5.ccls. and @ad<=20010328	USPAT;	2003/12/09
			US-PGPUB;	12:43
			EPO; JPO;	
			DERWENT;	
		716/6 1 0 1: 00010000	IBM_TDB	2002/20/20
-	561	716/6.ccls. and @ad<=20010328	USPAT; US-PGPUB;	2003/12/09
			EPO; JPO;	12.43
			DERWENT;	
			IBM TDB	
-	1401	"stress testing"	USPAT;	2003/12/10
			US-PGPUB;	08:38
			EPO; JPO;	
			DERWENT;	
	161	"stress testing" and ("IC" "integrated	IBM_TDB USPAT;	2003/12/10
1 -	461	"stress testing" and ("IC" "integrated   circuit" "\$5processor" "CPU" "memory	USPAT; US-PGPUB;	08:40
		controller" "I/O interface")	EPO; JPO;	00.30
		1,0 111011400 /	DERWENT;	
			IBM TDB	
				····

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-	366	("stress testing" and ("IC" "integrated circuit" "\$5processor" "CPU" "memory controller" "I/O interface")) and @ad<=20010328	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/12/10 08:40
-	1402	"stress testing"	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/12/11 15:44
-/ *	75	"stress testing" and bus and count and cycle	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/12/11 15:57
-	64	("stress testing" and bus and count and cycle) and @ad<=20010328	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/12/11   15:44
-	817	bus with count with cycle	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/12/11 15:57
_	343	(bus with count with cycle) and (test or debug)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/12/11 15:57
_	38702	bus and testing	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/12/12 08:32
_	827	(bus and testing) and cache adj line	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/12/12 08:33
_	5	((bus and testing) and cache adj line) and stress adj testing	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/12/12 08:42
-	34	adjacent adj cache adj line	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/12/12 08:43
_	27	(adjacent adj cache adj line) and @ad<=20010328	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/12/12 08:43
-	2	US-20020144183-\$.did.	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2004/03/23
-	279	714/37.ccls. and @ad<=20010328	DERWENT; IBM_TDB USPAT; US-PGPUB;	2004/03/23 12:57
-	495	714/30.ccls. and @ad<=20010328	EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2004/03/23
			DERWENT; IBM TDB	

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	259	714/39.ccls. and @ad<=20010328	USPAT;	2004/03/23
-	239	714/33.0013. and Gaux-20010320	US-PGPUB;	13:14
			EPO; JPO;	13.11
			DERWENT;	
			IBM TDB	
- 7	666	714/47.ccls. and @ad<=20010328	USPAT;	2004/03/23
		, as, as a same control of the contr	US-PGPUB;	13:17
			EPO; JPO;	
1			DERWENT;	
			IBM TDB	
7	382	714/43.ccls. and @ad<=20010328	USPĀT;	2004/03/23
			US-PGPUB;	13:26
			EPO; JPO;	ļ
/			DERWENT;	
			IBM TDB	
_	511	711/146.ccls. and @ad<=20010328	USPĀT;	2004/03/23
			US-PGPUB;	13:35
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	•
-	5	((, = 0, 0 , 0 0 = 0 , 0 0 0 0 0 0 0 0 0 0	USPĀT;	2004/03/23
		(716/6.ccls. and @ad<=20010328)) and "bus	US-PGPUB;	14:25
		transaction"	EPO; JPO;	
		·	DERWENT;	
			IBM_TDB	
-	9	trigger adj condition and bus adj	USPAT;	2004/03/23
		transaction	US-PGPUB;	14:35
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	121	trigger adj condition same bus	USPAT;	2004/03/23
			US-PGPUB;	14:35
			EPO; JPO;	
			DERWENT;	
	00	/*	<pre>IBM_TDB USPAT;</pre>	2004/03/24
-	99	(trigger adj condition same bus) and	USPAT; US-PGPUB;	10:11
		@ad<=20010328	EPO; JPO;	10:11
	•		DERWENT;	
			IBM TDB	
	0	stress adj test\$3 with trigger adj	USPAT;	2004/03/23
-	"	condition	US-PGPUB;	16:04
		Condition	EPO; JPO;	10.01
			DERWENT;	
			IBM TDB	
_	7	stress adj test\$3 and trigger adj	USPAT;	2004/03/23
		condition	US-PGPUB;	16:04
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1883	714/712.ccls. and @ad<=20010328	USPĀT;	2004/03/24
			US-PGPUB;	09:03
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	281	(714/712.ccls. and @ad<=20010328) and bus	USPAT;	2004/03/24
			US-PGPUB;	09:04
			EPO; JPO;	
			DERWENT;	
1		,, , ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	IBM_TDB	0004/02/54
-	384	(trigger adj condition and bus) and	USPAT;	2004/03/24
}		@ad<=20010328	US-PGPUB;	10:11
			EPO; JPO;	
			DERWENT; IBM TDB	
_	10	trigger adj condition) and (hit adj	USPAT;	2004/03/24
-	10	(trigger adj condition) and (nit adj   miss)	US-PGPUB;	15:51
		miss /	EPO; JPO;	10.01
			DERWENT;	
			IBM TDB	
	L	<u></u>		L